

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	62502	(power NEAR1 supply NEAR1 circuit) (CPU NEAR1 board)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/24 13:10
L3	125	(power NEAR1 supply NEAR1 circuit) AND (CPU NEAR1 board)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/24 13:14
L6	80	transformer AND (power NEAR1 supply NEAR1 switching NEAR1 circuit) AND layer	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/24 13:34
L7	44	(switching NEAR (control NEAR circuit) AND (rectifier) AND fet AND choke)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/24 14:33